



Docket No.: 250327US-2S

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RE: Application Serial No.: 10/798,403

Applicants: Shinichi YOSHIOKA

Filing Date: March 12, 2004

For: SEMICONDUCTOR INTEGRATED CIRCUIT  
DEVICE AND METHOD OF TESTING THE SAME

Group Art Unit: 2611

Examiner: FLORES, L.

SIR:

Attached hereto for filing are the following papers:

**PETITION TO WITHDRAW HOLDING OF ABANDONMENT  
FILING RECEIPT DATED JULY 23, 2007 (COPY)  
AMENDMENT W/ATTACHMENT (COPY)**

Our online credit card payment in the amount of **\$0.00** is being made covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time.

Respectfully submitted,

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